



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Arjun P. Chandran, Gregg K. Tsujimoto, and Anup S. Mehta
Assignee: Sun Microsystems, Inc.
Title: MODIFIED GLITCH LATCH FOR USE WITH POWER SAVING DYNAMIC REGISTER FILE STRUCTURES
Serial No.: 10/083,931 Filed: February 26, 2002
Examiner: Patel, Nitin C. Group Art 2116
Unit:
Docket No.: P-7433

Monterey, CA
July 25, 2005

Mail Stop Amendment
Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

In response to the Office Action dated February 24, 2005, please amend the above-identified application as follows:

1. Amendments to the Claims are reflected in the listing of Claims which begins on page 2 of this paper; and
2. Remarks begin on page 10 of this paper.